
Summary

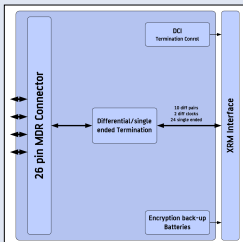
The **XRM-MDR** is an I/O Module which provides 24 LVDS/LVTTL I/O via the MDR 26pin connector. The XRM also provides battery support to provide a back-up supply for encryption key storage on the connected FPGA board.

Features
Applications:

General purpose I/O Interface

Front Connector I/O:

25 discrete I/O Interface



Specification

Product Name	XRM-MDR
Front I/O	Digital I/O: - 25 discrete I/O Interface connector=SCSI-2 68pin connector levels=Supports LVDS, LVTTTL, LVPECL, LVCMOS(3.3V and 2.5V signaling levels)†† Levels depend on FPGA board XRM is attached to Supports Xilinx™ Digitally Controlled Impedance (DCI)
Software	Example UCF, HDL files and Application software are provided with the board.
Battery	The XRM provides holders for two back-up batteries. Uses SR60 or equivalent batteries.
Environmental	Temperature: Operating Temperature 0° to +55°C EMC: FCC 47CFR Part 2 EN55022 Equipment Class B

Ordering Codes
XRM-MDR

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