



## Discrete I/O

## Board Features

- VHDCI (SCSI-3) 68-pin Connector
- Level Shifting on IO signals (Direct from FPGA)

## Summary

The **FMC-IO68** is an FMC I/O Module which provides a discrete I/O Interface.

The FMC-IO68 is a VITA 57.1 compliant single width HPC FMC module, designed for use with Alpha Data's VITA 57.1 compliant carrier cards. It provides the user with 68 IO signals to an industry standard SCSI connector.

## Deliverables

FMC-IO68 Board  
 One Year Warranty  
 One Year Technical Support

## Board Format

VITA 57.1 FPGA Mezzanine Card (FMC)

## Input/Output Interfaces

68x single ended IO (or (34 differential pairs)

2x RS232/RS422 Links

4x +3V3 CMOS I/O

6x Direct FPGA Links



**Support**

FMC Pinout available in User Manual

**Environmental Specification****Temperature Ranges**

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
ACE	0°C	70°C	-55°C	100°C

Operating Humidity : Up to 95% (non-condensing)

**EMC Standards**

FCC 47CFR Part 2

EN55022:2010 Equipment ClassB

**Ordering Information****Order Code: FMC-IO68(v)**

Option	Code	Description of Options
IO Voltage	v	blank = Direct from FPGA, /3V3 = 3.3V, /5V0 = 5.0V

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