

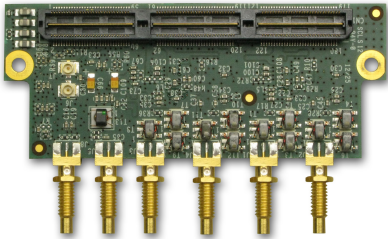
AD01250

### Prime Application

IF/Baseband Signal Sampling

### Board Features

- Quad 14-bit 250Msps ADCs
- External Clock Input
- Thermal monitoring of the ADCs



### Summary

The **XRM2-ADC-Q8/250** is an XRM2 I/O Module, providing four Analog to Digital converters with 14-bit resolution at sampling rates up to 250Msps.

Aimed at IF/Baseband Signal Sampling, the sampling clock can be sourced from either an external clock source or from a clock generated within the attached FPGA board. The Trigger I/O port is provided for use as a trigger input and general purpose signaling. An additional two ports are available for use as high-speed interconnect between boards for synchronisation. The built-in thermal monitor allows the user to check the operating temperature of the ADC. Provided as part of the sample design is the functionality to read the temperature of the device, and software to monitor this and recalibrate the ADC if the thermal drift is sufficient. The software will also shut the ADC down if the device starts to go over the maximum operating temperature.

### Deliverables

XRM2-ADC-Q8/250 Board  
One Year Warranty  
One Year Technical Support

### Board Format

Alpha Data XRM2 I/O Module

### Input/Output Interfaces

#### ADC

Quad Analog to Digital Converters

Resolution: 14-bit

Max Sample Freq: 40MHz to 250Msps

Bandwidth: 4.5MHz to 700MHz

Impedance: 50Ω

Connector: SSMC

#### External clock input

External Clock Input

#### Trigger I/O

Trigger I/O

#### Synchronisation I/O

Synchronisation I/O

### Environmental Specification

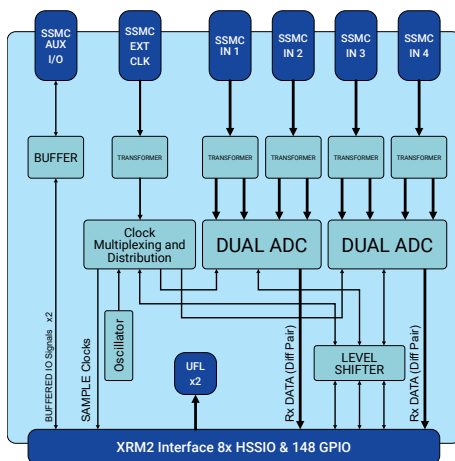
Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC0	0°C	+55°C	-40°C	+85°C

### Operating Humidity

Up to 95% (non-condensing)

### EMC Standards

FCC 47CFR Part 2  
EN55022:2010 Equipment ClassB



### Ordering Information

Order Code: XRM2-ADC-Q8/250