

**Summary**

The **ADM-PCIE-7V3** is a high performance reconfigurable Half-Length, low profile x16 PCIe form factor board based on the Xilinx™ Virtex-7 range of Platform FPGAs. The PCI Express® interface shall operate as two Gen 3 x8 end points (in compatible systems). The ADM-PCIE-7V3 features two independent channels of DDR3 memory capable of 1600MT/s (fitted with two 8GB SODIMMs), high speed I/O, SATA connections, dual SFP+ ports supporting 10G Ethernet, voltage/temperature/current control and monitoring, passive air-cooled heat sink.

Features**Applications:**

Data Processing
System Modelling
Market Analysis

Target Devices:

Xilinx Virtex-7: XC7VX690T-2 (FFG1157C)

Memory:

ECC-SODIMM - Dual 204-pin SODIMM Sockets for memory speeds up to 1600MT/s: Fitted with two 8GB SODIMMs at the factory.

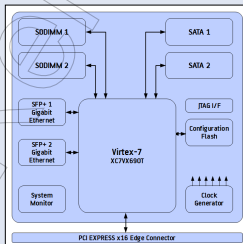
FLASH - 1GBit of BP1 x 16 configuration flash

Front Connector I/O:

Dual SFP+ Cages for 10Gigabit Ethernet

Other I/O:

Dual 6pin SATA Interfaces





Specification

Product Name	ADM-PCIE-7V3-16
Target Devices	Xilinx Virtex-7 - XC7VX690T-2 (FFG1157C)
Host I/F	PCI Express® Gen3 x8 (in x16 slot)
Interface	PCI Express® Gen3 x8 (2 off)
Memory	ECC-SODIMM - Dual 204-pin SODIMM Sockets for memory speeds up to 1600MT/s. Fitted with two 8GB SODIMMs at the factory. FLASH - 1GBit of BPI x 16 configuration flash
Front I/O	Dual SFP+ Cages for 10Gigabit Ethernet
Other I/O	Dual 6pin SATA Interfaces
Special Functions	Dual SFP+ cages for high speed optical communication including 10Gigabit Ethernet. Dual SATA Interfaces
Clocks	All Clocks derived from a local 100MHz LVDS oscillator with 20ppm 2x 100MHz - PCI Express® Clocks and user design 2x 400MHz - Memory Interfaces and user design 1x 200MHz - IODELAY reference 1x 156.25MHz - 10GbE SFP 1x 150MHz - SATA Interface 1x 100MHz - User Design 1x 250MHz - User design 1x 80MHz - FPGA Configuration Clock
Device Configuration	FPGA Configured via the on board Configuration Flash Flash programmed via JTAG
Software	Comprehensive Software Development Kit with source code for example software and FPGA designs.
Environmental	Temperature: Operating Temperature 0° to +55°C EMC: FCC 47CFR Part 2 EN55022 Equipment Class B

Ordering Codes

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