Features

Applications:

High Speed Network Communications

Front Connector I/O:

Dual 10GBase-SR or 10GBase-CR Ethernet Interfaces.
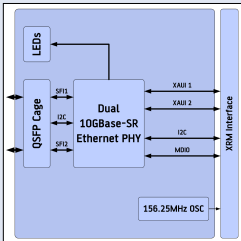
Rear Connector I/O:

The XRM provides a 156.25MHz LVPECL clock to the carrier PMC/XMC.
2x XAUI Interfaces
I2C Interface
MDIO Interface

Summary

The **XRM-10G-QSFP** is a front panel adapter card designed for use with Alpha Data's ADM-XRC-5, ADM-XRC-6, and ADM-XRC-7 FPGA based PMC/XMC cards.

The **XRM-10G-QSFP** will convert two 4 lane XAUI 10 Gigabit lanes into optical signals compliant with 10GBase-SR Ethernet.Provides dual 10GBase-SR Ethenet links connected to target FPGA via XAUI interfaces. The module also provides 10GBase-CR with direct attach Copper cabling.

Specification

Product Name	XRM-10G-QSFP
Front I/O	Dual 10GBase-SR or 10GBase-CR Ethernet Interfaces.
XRM2	The XRM-10G-QSFP is also available for XRM2 based FPGA products.
Rear I/O	The XRM provides a 156.25MHz LVPECL clock to the carrier PMC/XMC. 2x XAUI Interfaces I2C Interface MDIO Interface
Software	Example UCF and example design Application Note are provided with the board.
Environmental	**Temperature:** Operating Temperature 0° to +55°C **EMC:** FCC 47CFR Part 2 EN55022 Equipment Class B

Ordering Codes		
XRM(xver)-10G-QSFP		
XRM Version	xver	blank=Original XRM (FPGA products up to Virtex-5), 2=XRM Version 2 (FPGA products Virtex-6 and later)