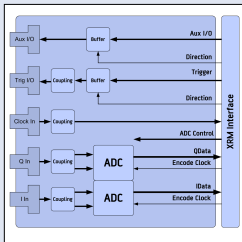## Features

Applications:

IF/Broadband Signal Sampling

Front Connector I/O:

Dual 14-bit ADC up to 125MHz

External Clock input

Trigger I/O Port

Auxiliary I/O port

Summary

The **XRM-ADC-D2/125** is an I/O Module which provides two 14-bit ADC channels at sampling rates up to 125Msps.The **XRM-ADC-D2/125** provides two channels of analog to digital conversion with 14 bit resolution and supports sampling rates up to 125MHz. It is aimed at applications such as IF/Baseband Signal Sampling.

A number of customisation options are offered with this card, ranging from signal input connector style through to transformer or DC-Coupling of inputs.

An external clock source or an internally generated clock may be used to provide the sampling clock.

Two auxiliary I/O ports are provided for use as trigger inputs and general purpose signals.

Specification

Product Name	XRM-ADC-D2/125
Front I/O	**Dual channel ADC:** Dual 14-bit ADC up to 125MHz resolution = 14-bit fmax = 125MHz bandwidth = AC Coupled = 10Hz to 200MHz DC Coupled = DC to 200MHz levels = Range1: ±1V = ADC Full scale impedance = 50 Ohms connector = SMA SMA-L SMB SMC **External Clock input:** External Clock input levels = +12dBm nominal (2.5V pk to pk) impedance = 50 Ohms (ac coupled) **Trig IO Port:** Trigger I/O Port levels = +3V3 LVTTL or +5V TTL (factory/user selectable)† impedance = Input: 4k7 Ohms (dc Coupled) †configured via 0R links **Aux IO Port:** Auxiliary I/O port levels = +3V3 LVTTL or +5V TTL (factory/user selectable)† †configured via 0R links
Software	Example UCF, HDL files and Application software are provided with the board.
Environmental	**Temperature:** Operating Temperature 0° to +55°C **EMC:** FCC 47CFR Part 2 EN55022 Equipment Class B

Ordering Codes

XRM(xver)-ADC-D2/125(coupling)(con)(io)		
XRM Version	xver	blank=Original XRM (FPGA products up to Virtex-5), 2=XRM Version 2 (FPGA products Virtex-6 and later)
Signal Coupling	coupling	blank = AC Coupled, /DC = DC Coupled
Connector Option	con	blank= SMA(7mm standard), /SMA20 = Long Barrel SMA(20mm), / SMB, /SMC
Voltage Option	io	blank = LVTTL I/O levels, /5V = TTL I/O Levels