Features

Front Connector I/O:

Dual 14-bit ADC up to 250Msps

External Clock Input
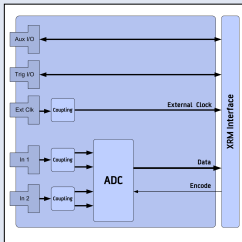
Trigger I/O port

Auxiliary I/O port

Summary

The **XRM-ADC-D6** is an I/O Module which provides two Analog to Digital converters with a sampling rate up to 250Msps.

High Speed Dual Channel ADC providing data at 14-bit per channel at up to 250Msps.

Specification

Product Name	XRM-ADC-D6/250

Front I/O	**Dual Channel ADC:** Dual 14-bit ADC up to 250Msps
	   resolution = 14-bit
	   levels = ±700mV
	   fmax = 250Msps
	   bandwidth = DC Coupled = DC to 450MHz
	   snr = 65dB
	   sfdr = 75dB
	   connector = SMA SMA-L SMB SMC
	External Clock: External Clock Input
	   impedance = 50Ω AC
	Trigger I/O: Trigger I/O port
	   type= "I/O" channels = 1
	   levels = TTL/LVTTL
	Auxiliary I/O: Auxiliary I/O port
	   type= "I/O" channels = 1
	   levels = TTL/LVTTL

XRM2	The XRM-ADC-D6/250 is also available for XRM2 based FPGA products.
Software	No special software required.
Environmental	**Temperature:** **Air cooled option (AC0)** Operating Temperature 0° to +55°C **Air cooled industrial option (AC1)** Operating Temperature -20° to +55°C **EMC:** FCC 47CFR Part 2 EN55022 Equipment Class B

Ordering Codes

XRM(xver)-ADC-D6(c)			
XRM Version	xv- er	blank=Original XRM (FPGA products up to Virtex-5), 2=XRM Version 2 (FPGA products Virtex-6 and later)	
Cooling	c	blank=Air Cooled Commercial, /AC1 = Air Cooled Industrial	