
**Summary**

The **XRC-DAC-D4/1G** is an I/O Module which provides two 16-bit Digital-to-Analog converters sampling at up to 1GHz.

It is aimed at IF/Baseband signal generation, provision is made for use of either an external or internally generated clock source. External trigger and auxiliary I/O ports are also provided.

**Features**
**Applications:**

IF/Baseband data signal generation

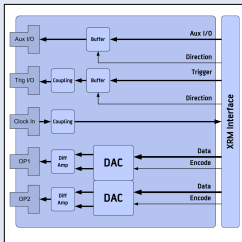
**Front Connector I/O:**

Dual 16-bit DACs up to 1GHz

External Clock Input

Trigger I/O port

Auxiliary I/O port





## Specification

Product Name	XRM-DAC-D4/1G
Front I/O	<p><b>Dual Channel DAC:</b> Dual 16-bit DACs up to 1GHz          resolution = 16-bit          levels = <math>\pm 1V</math> (open circuit)  <math>\pm 500mV</math> (50 load)  <math>f_{max} = 1000MHz</math>          bandwidth = 350MHz          connector = SMA SMA-L SMB SMC</p> <p><b>External Clock:</b> External Clock Input          impedance = 50 AC</p> <p><b>External Trigger I/O:</b> Trigger I/O port          levels = 3.3V LVTTTL          impedance = 4k<math>\Omega</math>, dc coupled          External Trigger signal</p> <p><b>External Auxiliary I/O:</b> Auxiliary I/O port          levels = 3.3V LVTTTL          impedance = 4k<math>\Omega</math>, dc coupled          External Auxiliary signal</p>
XRM2	The XRM-DAC-D4/1G is also available for XRM2 based FPGA products.
Software	No specific software required
Environmental	<p><b>Temperature:</b>          Operating Temperature 0° to +55°C</p> <p><b>EMC:</b>          FCC 47CFR Part 2          EN55022 Equipment Class B</p>

## Ordering Codes

XRM(xver)-DAC-D4/1G(con)(u)		
XRM Version	xver	blank = Original XRM (FPGA products up to Virtex-5), 2 = XRM Version 2 (FPGA products Virtex-6 and later)
Connector Option	con	blank = SMA (7mm standard), /SMA20 = Long Barrel SMA (20mm), /SMB, /SMC
XRM2 Keying	u	blank = keyed connector, /U = unkeyed connector

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