
Summary

The **XRM-DVI-D-RX** is an I/O Module which enables reception of a single channel of DVI data. The data is interfaced to the FPGA through a digital receiver chip, providing a wide parallel data stream to the FPGA. The XRM includes an EEPROM that will be connected to both the DDC signals on the DVI socket and an I2C interface on the XRM connector via a multiplexer. This allows the storing (via the attached FPGA board) and interrogation (by the source of the DVI data) of EDID data.

Features
Applications:

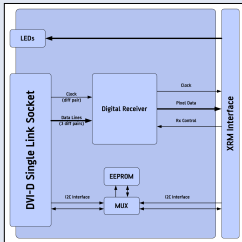
- Video Frame Grabber
- Image Processing
- Video Communications

Memory:

EEPROM - The EDID EEPROM is an I2C device. It can be programmed and verified from the FPGA and read through the DVI-D connector.

Front Connector I/O:

DVI Single Link Digital Video Receiver



Specification

Product Name	XRM-DVI-D-RX	
Memory	EEPROM - The EDID EEPROM is an I2C device. It can be programmed and verified from the FPGA and read through the DVI-D connector.	
Front I/O	DVI Input: - DVI Single Link Digital Video Receiver connector=DVI-D	
Special Functions	DVI Capture (25-165MHz)	8-bit per channel RGB) EDID EEPROM
Software	Please contact Alpha Data for availability of example software.	
Environmental	Temperature: Operating Temperature 0° to +55°C EMC: FCC 47CFR Part 2 EN55022 Equipment Class B	

Ordering Codes
XRM-DVI-D-RX

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