
**Summary**

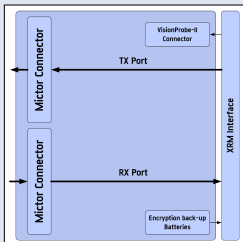
The **XRM-RIO** is an I/O Module which provides dual Parallel RapidIO ports. The XRM provides the Parallel RapidIO ports on Micror connectors.

**Features**
**Applications:**

General purpose Parallel RapidIO Interface

**Front Connector I/O:**

Dual Parallel RapidIO interfaces



**Specification**

Product Name	XRM-RIO
Front I/O	<b>RapidIO:</b> - Dual Parallel RapidIO interfaces levels=LVDS
Software	Compatible with Xilinx™ RapidIO Core
Battery	The XRM provides holders for two back-up batteries. Uses SR60 or equivalent batteries.
Environmental	<b>Temperature:</b> Operating Temperature 0° to +55°C  <b>EMC:</b> FCC 47CFR Part 2 EN55022 Equipment Class B

**Ordering Codes**
**XRM(xver)-RIO**

XRM Version	xver	blank=Original XRM (FPGA products up to Virtex-5), 2=XRM Version 2 (FPGA products Virtex-6 and later)
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