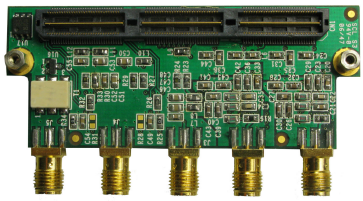
XRM2-ADC-D2/125

Datasheet Revision: 1.0
9th April 2019

AD01181



Prime Application

IF/Baseband Signal Sampling

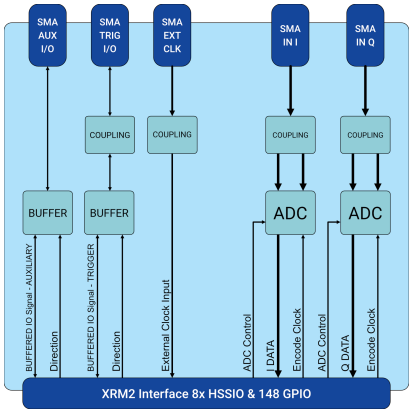
Board Features

- Dual 14-bit 125Msps ADCs
- External Clock Input

Summary

The **XRM2-ADC-D2/125** is an XRM2 I/O Module, providing two Analog to Digital converters with 14-bit resolution and sampling rates up to 125Msps.

Aimed at IF/Baseband Signal Sampling, the sampling clock can be sourced from either an external clock source or from a clock generated within the attached FPGA board. A Trigger I/O port is provided for use as a trigger control and an Auxiliary I/O port for general purpose signaling.
A number of customisation options are offered with this card, ranging from signal input connector style through to transformer or DC-Coupling of inputs.



Deliverables

XRM2-ADC-D2/125 Board
One Year Warranty
One Year Technical Support

Board Format

Alpha Data XRM2 I/O Module

Input/Output Interfaces

ADC
Dual Analog to Digital Converters

Resolution: 14-bit
Max Sample Freq: 125Msps
Bandwidth: AC Coupled = 10Hz to 200MHz
DC Coupled = DC to 200MHz
Impedance: 50Ω
Connector: SMA

External clock input
External Clock input

Trigger I/O
Trigger I/O

Auxiliary I/O
Auxiliary I/O

Environmental Specification

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC0	0°C	55°C	-40°C	85°C

Operating Humidity

Up to 95% (non-condensing)

EMC Standards

FCC 47CFR Part 2
EN55022:2010 Equipment ClassB

Ordering Information

Order Code: XRM2-ADC-D2/125(coupling)

Option	Code	Description of Options
Signal Coupling	coupling	blank = AC Coupled, /DC = DC Coupled

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