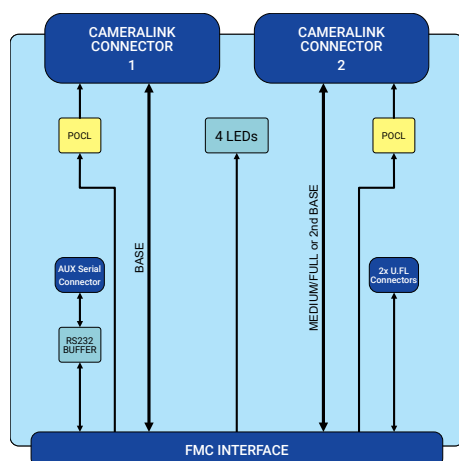
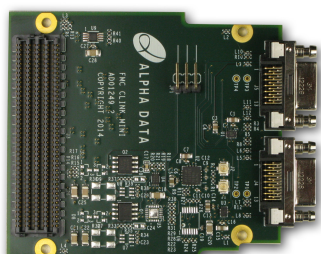


AD01249



Applications

- Camera Link I/O Interface
- Video Frame Grabber
- Image Processing
- Video Communications

Board Features

- Multiple CameraLink Modes

Summary

The **FMC-CAMERALINK** is an FMC I/O Module which provides an industry standard CameraLink interface.

It provides the user with the ability to implement computationally-intensive applications such as frame grabbers, digital video communications and image processing systems in the FPGA when using a compatible camera.

The **FMC-CAMERALINK** board supports 2x Base Cameras, 1x Medium/Full Camera and each option with or without Power over CameraLink (PoCL). Full user control of the standard camera control lines and serial interface are provided.

The **FMC-CAMERALINK** utilizes a Low Pin Count (LPC) interface, compatible with both LPC and HPC interfaces.

Board Format

VITA 57.1 FPGA Mezzanine Card (FMC)

Input/Output Interfaces

CameraLink Interface (Front Panel)

2x SDR Connectors - Providing single and dual CameraLink modes

Environmental Specification

Cooling	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
Option				
AC0	0°C	+55°C	-40°C	+85°C

Operating Humidity

Up to 95% (non-condensing)

Order Code: FMC-CAMERALINK(p)(c)

PoCL	p	blank = NO power supplied over the Camera Link Connection, /POCL = Power Is provided over the Camera Link Connection
Custom	c	blank = 1.8V-3.3V standard support, /C1 = 1.5V support (for versal, no PoCL, no serial)