

AD01265

Applications

- PCI Express Gen 3 Interface
- 10Gb Ethernet
- other High Speed IO Protocols

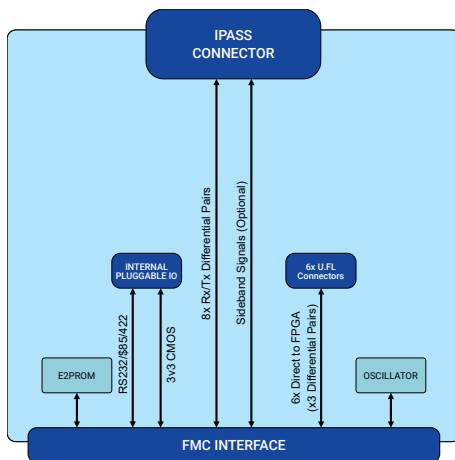
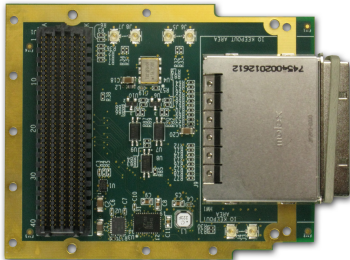
Board Features

- iPASS Connector

Summary

The **FMC-IPASS** is an FMC I/O Module which provides an industry standard High-Speed Serial interface.

The FMC-IPASS is a VITA 57.1 compliant Single Width HPC FMC module, designed for use with Alpha Data's VITA 57.1 compliant carrier cards. It provides the user with the connectivity to implement high-speed serial IO communication applications. Providing 8 lanes of up to 12Gbps Serdes capable of PCIe gen3, 10GbE, and many other signaling standards.



Deliverables

FMC-IPASS Board
One Year Warranty
One Year Technical Support

Board Format

VITA 57.1 FPGA Mezzanine Card (FMC)

Input/Output Interfaces

High-speed Serial IO

8 lanes of up to 12Gbps Serdes capable of PCIe gen3

Low-speed Serial IO

Serial Communications

Discrete IO

GPIO

Environmental Specification

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
CC1	-40°C	+70°C	-55°C	+100°C

Operating Humidity

Up to 95% (non-condensing)

EMC Standards

FCC 47CFR Part 2
EN55022:2010 Equipment ClassB

Ordering Information

Order Code: FMC-IPASS(f)

Option	Code	Description of Options
Freq	f	blank = 156.25MHz, /125 = 125MHz, /### = ###MHz