

AD01199

## Prime Application

IF/Baseband Signal Sampling

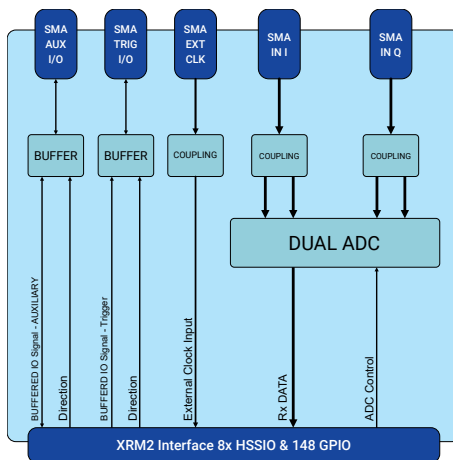
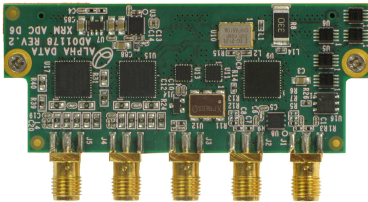
## Board Features

- Dual 14-bit 250Msps ADCs
- External Clock Input

## Summary

The **XRM2-ADC-D6/250** is an XRM2 I/O Module, providing two Analog to Digital converters with 14-bit resolution and sampling rates up to 250Msps.

Aimed at IF/Baseband Signal Sampling, the sampling clock can be sourced from either an external clock source or from a clock generated within the attached FPGA board. A Trigger I/O port is provided for use as a trigger control and an Auxiliary I/O port for general purpose signaling.



## Deliverables

XRM2-ADC-D6/250 Board  
One Year Warranty  
One Year Technical Support

## Board Format

Alpha Data XRM2 I/O Module

## Input/Output Interfaces

### ADC

Dual Analog to Digital Converters

Resolution: 14-bit

Max Sample Freq: 250Msps

Bandwidth: DC Coupled = DC to 450MHz

Impedance: 50Ω

Connector: SMA

### External clock input

External Clock Input

### Trigger I/O

Trigger I/O

### Auxiliary I/O

Auxiliary I/O

## Environmental Specification

| Cooling Option | Operating Temperatures |       | Storage Temperatures |        |
|----------------|------------------------|-------|----------------------|--------|
|                | Min                    | Max   | Min                  | Max    |
| AC0            | 0°C                    | +55°C | -40°C                | +85°C  |
| AC1            | -40°C                  | +70°C | -55°C                | +100°C |

## Operating Humidity

Up to 95% (non-condensing)

## EMC Standards

FCC 47CFR Part 2

EN55022:2010 Equipment ClassB

## Ordering Information

**Order Code: XRM2-ADC-D6/250(c)**

| Option  | Code | Description of Options                                    |
|---------|------|---|
| Cooling | c    | blank=Air Cooled Commercial, /AC1 = Air Cooled Industrial |

## UK Office

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