AD01302

Applications

- RF/IF Signal Sampling
- High-speed Analog signal Sampling

Board Features

- Single 12-bit 2000/2500Msps ADC
- External Clock Input

Summary

The **XRM2-ADC-S11** is an XRM2 I/O Module, providing one Analog to Digital converter with 12-bit resolution and sampling rates up to 2000Msps (2500Msps with order code option /25).

Aimed at IF/RF Signal Sampling, the sampling clock can be sourced from either an external clock source or from a clock generated within the attached FPGA board. A programmable input attenuator can be used to vary the full scale input level over a 15 dB range. An Auxiliary I/O port is provided for use as a trigger input and general purpose signaling. The XRM2 communicates to the FPGA via a JESD204B high speed interface.
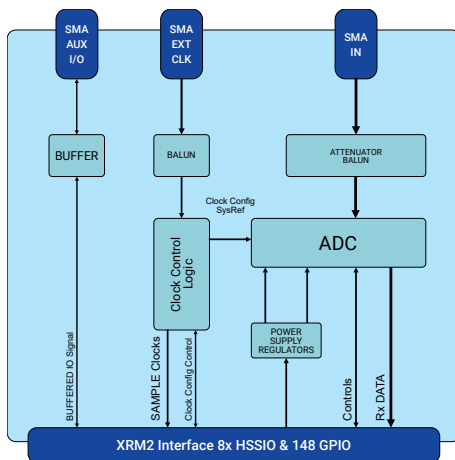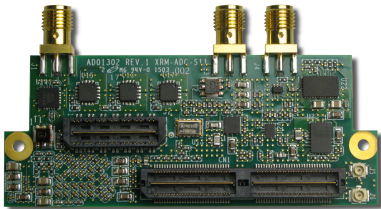
Deliverables

XRM2-ADC-S11 Board
One Year Warranty
One Year Technical Support

Board Format

Alpha Data XRM2 I/O Module

Input/Output Interfaces

ADC
Single Analog to Digital Converter

Resolution: 12-bit
Max Sample Freq: 2000Msps (2500Msps with ordering option)
Bandwidth: (-3dB) 10MHz to 3000MHz
Impedance: 50Ω
Connector: SMA

External clock input
External clock input

Auxiliary I/O
Auxiliary I/O

Environmental Specification

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC1	-40°C	+70°C	-55°C	+100°C

Operating Humidity

Up to 95% (non-condensing)

EMC Standards

FCC 47CFR Part 2
EN55022:2010 Equipment ClassB

Ordering Information			
Order Code: XRM2-ADC-S11(sp)			
Option	Code	Description of Options	
Sampling Speed	sp	blank = 2000Msps, /25 = 2500Msps	