

XRM2-DAC-D4/1G

Datasheet Revision: 3.0 1st November 2020

AD01152



Prime Application

IF/Baseband data signal generation

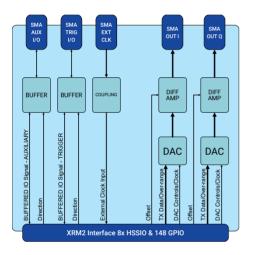
Board Features

- · Dual 16-bit 1Gsps DACs
- External Clock Input

Summary

The XRM2-DAC-D4/1G is an XRM2 I/O Module, providing two Digital to Analog converters with 16-bit resolution and sampling rates up to 1GHz.

Aimed at IF/Baseband signal generation, provision is made for use of either an external or internally generated clock source. External trigger and auxiliary I/O ports are also provided.



Deliverables

XRM2-DAC-D4/1G Board One Year Warranty One Year Technical Support

Board Format

Alpha Data XRM2 I/O Module

Input/Output Interfaces

DAC

Dual 16-bit DACs up to 1GHz

Resolution: 16-bit

Max Sample Freq: 1000MHz Bandwidth: 1MHz to 350MHz Impedance: 50Ω (AC coupled)

Connector: SMA

External clock input External clock input

Trigger I/O

Trigger I/O

Auxiliary I/O

Auxiliary I/O

Synchronisation I/O

Synchronisation I/O

Environmental Specification

Cooling	Ope	rating	Storage	
Option	Temperatures		Temperatures	
	Min	Max	Min	Max
AC0	0°C	+55°C	-40°C	+85°C

Operating Humidity

Up to 95% (non-condensing)

EMC Standards

FCC 47CFR Part 2 EN55022:2010 Equipment ClassB

Ordering Information					
Order Code: XRM2-DAC-D4/1G(u)					
Option	Code	Description of Options			
XRM2 Keying	u	blank = keyed connector, /U = unkeyed connector			



Address: