

AD01105

Prime Application

General purpose I/O Interface

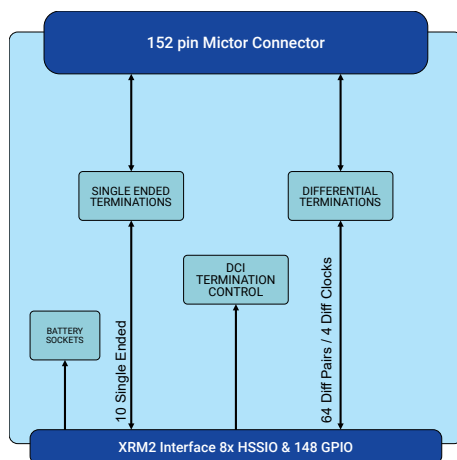
Board Features

- 146 Digital I/O

Summary

The **XRM2-IO146** is an XRM2 I/O Module which provides up to 146 LVDS/LVTTL I/O via a Mictor 152pin connector.

The XRM2-IO146 also provides battery support to provide a back-up supply for encryption key storage on the connected FPGA board.



Board Format

Alpha Data XRM2 I/O Module

Input/Output Interfaces

Digital I/O (Front Panel)

64x differential pairs
10x Single ended I/O
4x differential pairs clock pins

Environmental Specification

Cooling	Operating Temperatures		Storage Temperatures	
Option	Min	Max	Min	Max
AC1	-40°C	+85°C	-55°C	+100°C

Operating Humidity

Up to 95% (non-condensing)

Order Code: XRM2-IO146(LOCK)(EXTRA)

Custom Modes	EXTRA	blank=No other options, -[code]=Other build options for custom products
Cable Lock	LOCK	blank=No locking mechanism, -LOCK=Cable locking assembly fitted
NOTE	If the XRM2-IO146 is to be used to supply 5V indicate this in your order so the appropriate components can be fitted.	