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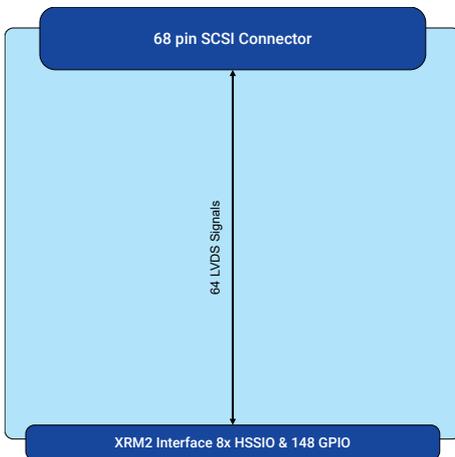
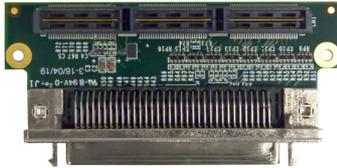
**Prime Application****General purpose I/O Interface****Board Features**

- SCSI-2 68-pin connector

**Summary**

The **XRM2-IO64** is an XRM2 I/O Module which provides up to 64 LVDS/LVTTL I/O via the SCSI-2 68pin connector.

The XRM2-IO64 also provides battery support to provide a back-up supply for encryption key storage on the connected FPGA board.

**Deliverables**

XRM2-IO64 Board  
One Year Warranty  
One Year Technical Support

**Input/Output Interfaces**

**Digital I/O**  
discrete digital I/O LVDS

**UK Office**

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**Support**

Example UCF, HDL files and Application software are provided with the board.

**Board Format**

Alpha Data XRM2 I/O Module	ERROR	ERROR	ERROR	ERROR
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**Environmental Specification**

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC1	-40°C	+70°C	-55°C	+100°C

Operating Humidity : Up to 95% (non-condensing)

**EMC Standards**

FCC 47CFR Part 2  
EN55022:2010 Equipment ClassB

**Ordering Information**

**Order Code: XRM2-IO64**