

ITT 1637

## **Prime Application**

**SpaceWire Communications** 



### **Board Features**

- 2-bit Serializer/Deserializer (SERDES) functionality
- LVDS Physical Layer
- Data rates to 200 Mbits/sec
- Data/Strobe transmit skew <500pS
- Each channel connected via 9-pin microD receptacle - compatible with STAR-Dundee cables

The XRM2-SPW is an I/O Module which provides SpaceWire Interface capability for Alpha Data FPGA cards.

It provides the user with the ability to implement two SpaceWire Links.The links are Fully Isolated from the FPGA board (>1.0kV isolation rating) with power provided by a front panel connector.

#### **Deliverables**

Summary

XRM2-SPW Board One Year Warranty One Year Technical Support

# **Host Interface**

# **Board Format**

### **Input/Output Interfaces**

SpaceWire Transceiver

# **Environmental Specification**

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC1	-40°C	+70°C	-55°C	+100°C
CC1	-40°C	+70°C	-55°C	+100°C

**Operating Humidity** 

Up to 95% (non-condensing)

### **EMC Standards**

FCC 47CFR Part 2 EN55022:2010 Equipment ClassB

**Ordering Information** 

### Order Code: XRM2-SPW/U



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